

Amended Version of Specification Showing Changes

(Paragraph on page 4, lines 9-18.)

In general, for the embodiment of Fig. 4b and other embodiments, vias **408** define a regular array. The region between two consecutive rows (or columns) of a regular array of vias [define] defines a channel. For the embodiment of Fig. 4b and other embodiments, bus lines are routed so as to be within or underneath one and only one channel. That is, in an area or region of a board directly underneath [a] an agent that is to be connected to a bus, individual bus traces making up the bus do not cross from one channel to the next. For the particular embodiment shown in Fig. 4b, bus traces **410** and **412** are in one channel, and bus traces **414** and **416** are in another channel. Bus topologies such as those according to the embodiments of Figs. 3, 4a, and 4b allow for relatively small stub lengths, and are found to address some or all of the problems cited in the Background.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231

on _____

8/22/01
Date of Deposit

D. Roberts
Name of Person Mailing Correspondence

D. Roberts
Signature

8/22/01
Date